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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Robert D. Norman	
	Filing Date: Herewith	Group: Unknown 2186

U. S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
BJP	5,029,209	07/02/1991	Strong, Jr., F.S., et al	380	25	06/13/90
BJP	5,293,498	03/08/1994	Iwatsubo, M.	395	325	04/19/93
BJP	5,430,859	07/04/1995	Norman, R.D., et al.	395	425	07/26/91
BJP	5,627,784	05/06/1997	Roohparvar, F.F.	365	189.01	07/28/95
BJP	5,640,332	06/17/1997	Baker, D.C., et al.	364	514A	05/16/96
BJP	5,687,117	11/11/1997	Chevallier, C.J., et al.	365	185.12	02/23/96
BJP	5,873,123	02/16/1999	Patel, R.B., et al.	711	202	06/25/96

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

BJP	"Draft Standard for A High-Speed Memory Interface (SyncLink)", <u>Microprocessor and Microcomputer Standards Subcommittee of the IEEE Computer Society</u> , New York: Institute of Electrical and Electronics Engineers, Inc., 1-52, (1996)
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Examiner

Petkari

Date Considered

12/15/02

*Substitute Disclosure Statement Form (PTO-1449)

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